

Notice of References Cited

Application/Control No.

09/688,021

Applicant(s)/Patent Under

Reexamination

ANNAPRAGADA ET AL.

Examiner

Matthew A. Anderson

Art Unit

1765

Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5362663	11-1994	Bronner et al.	437/52
	B	US-6380096 B2	04-2002	Hung et al.	438/723
	C	US-6211092 B1	04-2001	Tang et al.	438/719
	D	US-6074959	06-2000	Wang et al.	438/738
	E	US-6180518 B1	01-2001	Layadi et al.	438/639
	F	US-6174451 B1	01-2001	Hung et al.	438/723
	G	US-5942446	08-1999	Chen et al.	438/734
	H	US-6284149 B1	09-2001	Li et al.	216/64
	I	US-5739579	04-1998	Chiang et al.	257/635
	J	US-6387287 B1	05-2002	Hung et al.	216/67
	K	US-4161743	07-1979	Yonezawa et al.	357/4
	L	US-6228774 B1	05-2001	Marquez	438/710
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf et al., Silcion Processing for the VLSI Era Volume 1: Process Technology, Lattice Press, Sunset Beach, CA, USA, pp. 539-540, 546-558, 1986.
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.